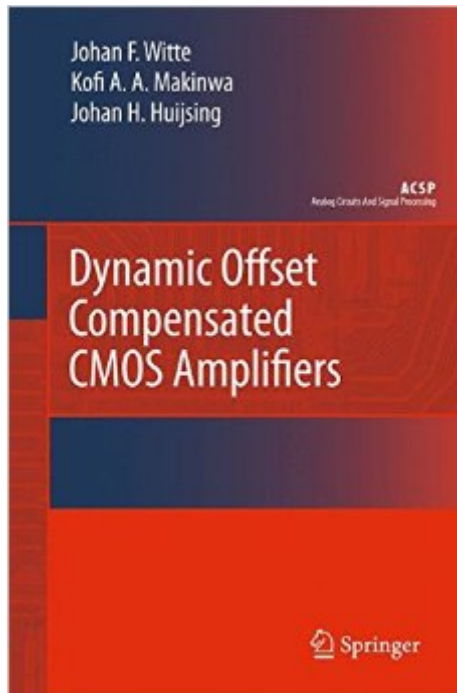# Dynamic Offset Compensated CMOS Amplifiers (Analog Circuits And Signal Processing)

Synopsis

Dynamic Offset-Compensated CMOS Amplifiers describes the theory, design and realization of dynamic offset compensated CMOS amplifiers. It focuses on the design of general-purpose wide-band operational amplifiers and instrumentation amplifiers.Two offset compensation techniques are described: auto-zeroing and chopping. Several topologies are discussed, with which these techniques can be used in the design of wide-band dynamic offset-compensated amplifiers. Four implementations are discussed in detail: two low-offset wide-band operational amplifiers, a low-offset instrumentation amplifier, and a low-offset current-sense amplifier, which can sense the current drawn from supply voltages up to 28V .

Book Information

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